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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/677,487	10/03/2003	Takumi Seki	116756.01	4568
25944	7590	11/01/2005	EXAMINER	
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			TON, MINH TOAN T	
			ART UNIT	PAPER NUMBER
			2871	

DATE MAILED: 11/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

H.D

Office Action Summary	Application No.	Applicant(s)	
	10/677,487	SEKI ET AL.	
	Examiner	Art Unit	
	Toan Ton	2871	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1,3 and 4 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1,3 and 4 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. ____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1, 3-4 are rejected under 35 U.S.C. 102(e) as being anticipated by Ha et al (US 6654076).

Ha discloses a transfective liquid crystal display (LCD) device comprising (see at least Figures 4-5): a liquid crystal material disposed between an element substrate 110 having pixel electrodes and an opposing substrate facing the element substrate; the element substrate including semiconductor elements (TFTs) that drive the pixel electrodes, insulation films (e.g., 170, 190) covering the semiconductor elements, a reflective plate 180 disposed on the insulation films, the reflective plate having openings; each of the semiconductor elements adjacent to the element substrate having a light-shielding layer (125/145) that shields semiconductor element from incident light, the light-shielding layer having openings (172,182) in substantially the same regions as those of the openings in the reflective plate.

Ha discloses the source line being covered by one of the pixel electrodes to avoid contact with one of the openings of the light-shielding layer (see at least Figures 4-5).

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3. Claims 1, 3-4 are rejected under 35 U.S.C. 102(b) as being anticipated by Tetsuro (JP 2001-3183877).

Tetsuro discloses a transfective liquid crystal display (LCD) device comprising (see at least Figure 1): a liquid crystal material 15 disposed between an element substrate having pixel electrodes and an opposing substrate facing the element substrate; the element substrate 2 including semiconductor elements 3 (TFTs) that drive the pixel electrodes, insulation films (e.g., 9, 10, 12) covering the semiconductor elements, a reflective plate 14 disposed on the insulation films, the reflective plate having openings; each of the semiconductor elements adjacent to the element substrate having a light-shielding layer 30 that shields semiconductor element from incident light, the light-shielding layer having openings (T) in substantially the same regions as those of the openings in the reflective plate.

Ha discloses the source line being covered by one of the pixel electrodes to avoid contact with one of the openings of the light-shielding layer (see at least Figure 1).


Contact Information

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Toan Ton whose telephone number is (571) 272-2303.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

October 27, 2005


TOANTON
PRIMARY EXAMINER